

# A Novel Three-Phase Low Voltage (LV) Dynamic Voltage Restorer (DVR) Employing Semi-Z-Source Inverter

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**Abstract**— This paper presents the design and control of a Low Voltage (LV) Dynamic Voltage Restorer (DVR) for a three-phase power system. The DVR is designed to protect a three-phase load of 3 kVA from the voltage sags and swells ranging up to 0.5 p.u of the supply voltage. The proposed DVR utilizes semi-Z-source (Szs) inverter as the power converter in each phase of the DVR. The SzS inverter offers many advantages such as reduction in cost, size, weight, elimination of filter etc. compared to the conventional Full Bridge (FB) inverter based DVR. Each phase is controlled independently which offers mitigation of both symmetrical and asymmetrical sags. The proposed DVR employs the modified feedback control technique. The simulation results from MATLAB/Simulink environment verify the efficacy of the proposed DVR topology. The proposed DVR topology is suitable for mitigation of sags and swells initiating at any point-on-wave.

**Keywords**— Low Voltage Dynamic Voltage Restorer (LV DVR), Power Quality (PQ), Voltage Sag, Semi-Z-Source (Szs) inverter

## I. INTRODUCTION

Dynamic Voltage Restorer (DVR) is a promising solution to replace the costly uninterruptible power supplies in the low voltage power system. DVR is a cost-effective voltage compensation device which protects the sensitive and critical loads from Power Quality (PQ) issues such as voltage sags, swells, harmonics, flickers etc. A DVR is a series voltage compensator which adds or subtracts voltage to the supply depending on the PQ problem in order to maintain the load profile at the predefined values. The basic structure of the DVR consists of energy storage, power converter, series injection transformer and a crowbar or bypass circuit. The power converter synthesizes the missing voltage and with the aid of the injection transformer it is injected in series with the supply and the load. The by-pass equipment protects the DVR from short-circuiting currents and also connects or removes the DVR whenever required [1]–[5]

This paper introduces a novel LV DVR suitable for distribution line. The proposed LV DVR incorporates semi-Z-source (Szs) inverter as the power converter for synthesizing the missing voltage. The SzS inverter shares the same output voltage range with that of the FB inverter. With only two switches, the SzS inverter is capable of giving more sinusoidal output voltage compared to the FB inverter. The reduction in the number of switches, Z-source network in the ac-side of the inverter, no-shoot through condition, elimination of filter and dead-time control are some of the advantages of SzS inverter over the FB inverter [6], [7]. The circuit topology of the proposed three-phase SzS inverter based LV DVR is presented in Fig.1

The major requirements of an LV DVR such as low cost, small size and low weight are met satisfactorily by the proposed DVR. The compensation voltage of each phase is controlled independently in the proposed DVR topology. The merit of independent control of each phase offers the mitigation of both symmetrical and asymmetrical sags by the proposed topology compared to the three-phase DVR presented in [8]. The proposed DVR is employed with modified feedback control technique [9]. A single energy storage system supplies all the SzS inverters in the proposed DVR topology. The sharing of common energy storage among the three phases reduces the size, weight and cost of the proposed DVR. There is no possibility of energy storage short-circuiting as the switches are present in different legs. Both transformer-connected and transformer-less topology is possible with the proposed LV DVR.

This paper presents the protection of the critical load from balanced and unbalanced voltage sags using the proposed three-phase LV DVR. The following section gives design of the proposed DVR. The voltage sag detection method, reference generation and the control strategy are elaborated in the section III. The simulation results from the MATLAB/Simulink software validate the suitability of the proposed LV DVR for the tight restoration of load voltage

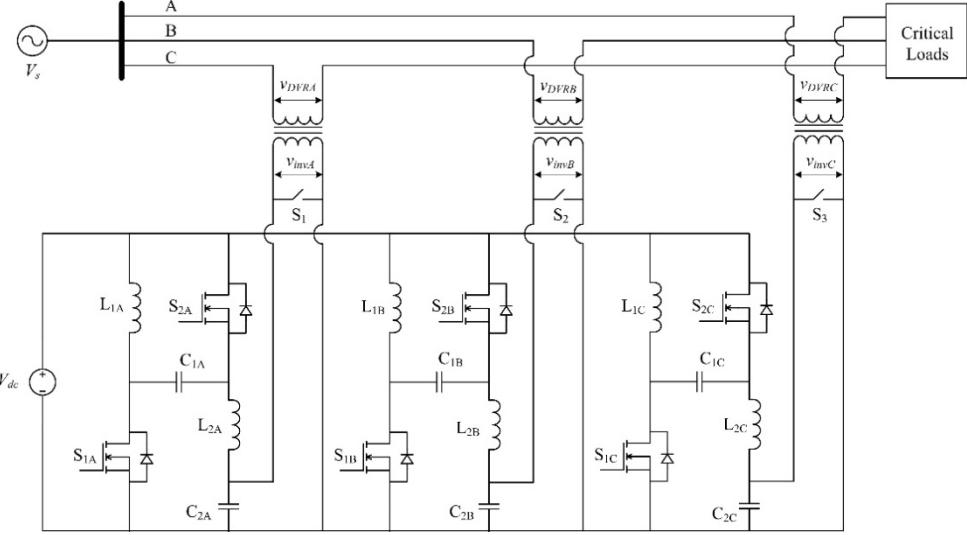


Fig.1 Proposed three-phase DVR employing SZS inverter.

during the PQ events.

## II. PROPOSED THREE-PHASE LV DVR BASED ON SEMI-Z-SOURCE INVERTER

The proposed three-phase LV DVR topology consists of three SZS inverters which share a common dc energy storage source  $V_{dc}$ . The SZS inverters are connected to each phase of the three-phase power system using separate injection transformers of turns ratio 1:1. The basic component of the proposed three-phase DVR system is the SZS dc-dc converter. Cao et.al proposed a buck dc-dc converter which can be operated as buck inverter by varying the duty cycle of the switches [10]. The SZS inverter consists of a Z-source network and two switches. The Z-source network is placed on the ac-side of the inverter, thereby reducing the size of the inductors and capacitors. The SZS converter shares the output voltage range same as the FB inverter with only two switches. The two switches are present in different legs [6], [10]–[12]. Owing to this, there is no risk of short-circuiting or need of dead-time control in the proposed DVR topology. The conventional LV DVR based on FB inverter requires dead-time control which adds fifth and seventh harmonics into the system [7].

MOSFET and IGBT are the regular choices of the SZS inverter switches as they fulfill the bidirectional current conduction and unidirectional voltage blocking property. With the reduction in the size of Z-source network and number of switches, the SZS inverter reduces the overall footprint of the proposed LV DVR compared to the FB inverter based DVR. The two switches in the SZS inverter are operated complementarily.

The SZS inverter in each phase generates the compensation voltage required in that particular phase to maintain the load profile constant. For the injection of the compensation voltage, the in-phase method is chosen which

injects the compensation voltage or DVR voltage in-phase with the supply voltage. The generated compensation voltage is added in series with the supply and the load with the help of series injection transformer. The injection transformer serves the purpose of galvanic isolation and also offers voltage boosting function [13]–[15]. In this proposed topology, the task of the injection transformer is to provide only isolation. The DVR system connected to each phase namely A, B and C operate independently and identically. Hence, the operation of DVR and its related components in each phase is similar. The letter ‘X’ is used as the subscript in voltage, current, duty cycle, inductor, capacitor and switch notations to denote which phase it belongs to. The letter ‘X’ can be A, B or C depending on the phase under consideration.  $V_{DVR}$  represents the three-phase compensation voltage and  $V_{DVRX}$  denotes the compensation voltage in X phase.

When the three-phase system is healthy, the proposed LV DVR is in stand-by mode and is removed from the line using the by-pass switch. The bypass switches are  $S_1$ ,  $S_2$  and  $S_3$  in phases A, B and C respectively. The proposed DVR shifts to the compensation mode under unhealthy supply conditions. Once the voltage sag is detected in a particular phase, the SZS inverter connected to that phase starts to synthesize the compensation voltage ( $V_{DVRX}$ ) according to the reference voltage ( $V_{DVRX,\text{ref}}$ ) generated. The SZS inverter output voltage ( $V_{invX}$ ) is same as the compensation voltage ( $V_{DVRX}$ ). The SZS inverter is capable of generating sinusoidal voltage by varying the duty cycle of switch  $S_{1X}$ . The voltage gain of the SZS inverter in the proposed DVR is given by equation (1)

$$\frac{\sqrt{2} \cdot V_{DVRX} \sin \omega t}{V_{dc}} = \frac{1 - 2D_{1X}}{1 - D_{1X}} \quad (1)$$

where  $D_{1X}$  represents the duty cycle of switch  $S_{1X}$ . The SZS

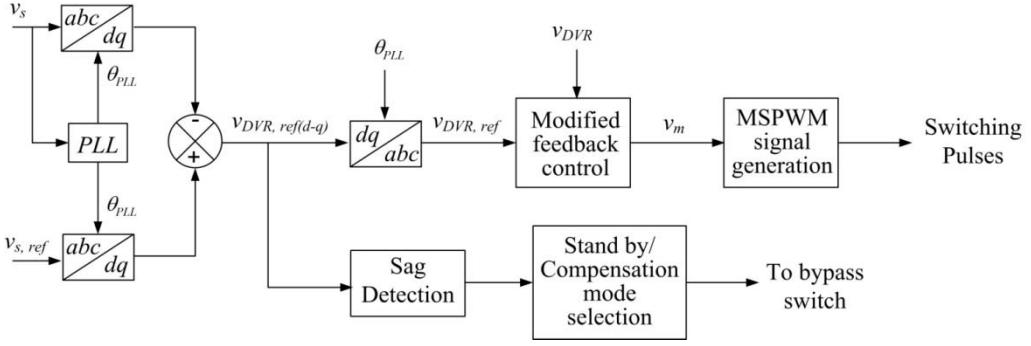


Fig.2 Control structure of the proposed LV DVR

inverter can output a sinusoidal voltage with peak voltage equal to  $V_{dc}$  by varying the duty cycle of switch  $S_{1X}$  from 0 to 0.6667. The SIZS inverter generates the positive half cycle of the output voltage by varying the duty cycle  $D_{1X}$  from 0 to 0.5 and negative half-cycle when the same is varied from 0.5 to 0.6667.

From the equation (1), it is clear that the voltage gain of the SIZS inverter is non-linear. This non-linearity in the voltage gain demands a modified reference voltage for the Sinusoidal Pulse Width Modulation (SPWM) of the switches in the SIZS inverter. The left-hand side of equation (1) can be rewritten as  $M_X \sin \omega t$  where  $M_X$  is the modulation index of the SIZS inverter under operation and  $\omega$  is the angular frequency of the three-phase system in radians. Rearranging equation (1), the duty cycle  $D_{1X}$  can be written as equation (2)

$$D_{1X} = \frac{1-M_X \sin \omega t}{2-M_X \sin \omega t} \quad (2)$$

which is the revised reference wave for the modified SPWM (MSPWM). For ease of implementation, the reference is normally generated for switch  $D_{2X}$  which requires less mathematical calculation and is given by equation (3)

$$D_{2X} = \frac{1}{2-M_X \sin \omega t} \quad (3)$$

The modes of operation and steady-state equations of the SIZS inverter is given in [10].

The proposed LV DVR is designed for 50% compensation capability. A safety margin of extra 20% is included in the design of dc voltage and transformer. The dc energy storage voltage rating is selected by following equation (4) where  $V_S$  is the line-to-line voltage of the three-phase system.

$$V_{dc} = 1.2 * 0.5 * \frac{V_S}{\sqrt{3}} * \sqrt{2} \quad (4)$$

The maximum peak value of the compensation voltage in each phase ( $V_{DVRX,max}$ ) of the proposed DVR is designed for 50% compensation capability and is given by equation (5)

$$V_{DVRX,max} = 0.5 * \frac{V_S}{\sqrt{3}} * \sqrt{2} \quad (5)$$

For the design of the Z-source network, the maximum modulation index ( $M_{Xmax}$ ) is calculated by dividing the equation (5) by equation (4). For the maximum modulation index obtained, duty cycle is calculated for the maximum positive voltage using equation (2) by substituting  $\omega t = \frac{\pi}{2}$  and  $M_X = M_{Xmax}$ . With the obtained duty cycle  $D_{1X}$ , the maximum possible capacitor voltage and inductor currents are calculated for the given load connected to the SIZS inverter. The capacitor voltage ripple and inductor current ripple in the SIZS inverter are given by equations (14) and (15) of [10] and are presented as equation (6) and (7) in the context of proposed DVR topology.

$$\Delta V_{C1X} = \frac{(1-D_{1X})T_s I_{L1X}}{C_{1X}} = \frac{-\sin \omega t + M_X (\sin \omega t)^2}{2-M_X \sin \omega t} \frac{T_s}{C_{1X}} I_X \quad (6)$$

$$\Delta I_{L1X} = \Delta I_{L2X} = \frac{V_{dc} T_s D_{1X}}{L_{1X}} = \frac{V_{dc} T_s}{L_{1X}} \frac{1-M_X \sin \omega t}{2-M_X \sin \omega t} \quad (7)$$

where  $\Delta V_{C1X}$ ,  $\Delta I_{L1X}$ ,  $\Delta I_{L2X}$  and  $T_s$  gives the capacitor  $C_{1X}$  voltage ripple, inductor  $L_{1X}$  current ripple, inductor  $L_{2X}$  current ripple and switching time period respectively. The peak value of the load current in each phase is represented by  $I_X$ . The value of both the capacitors and inductors are calculated using the equations (6) and (7) respectively. Furthermore, the injection transformer power rating is selected by taking into consideration the maximum

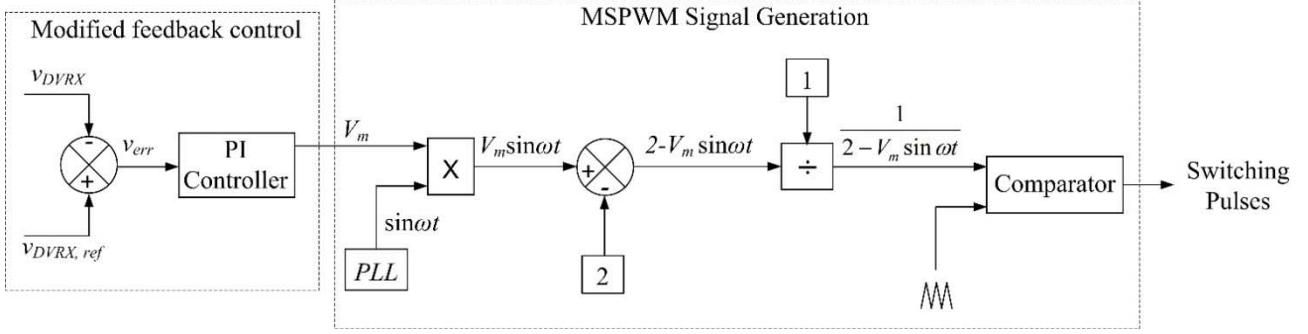


Fig.3 Modified feedback controller and MSPWM signal generation in phase X of the proposed LV DVR

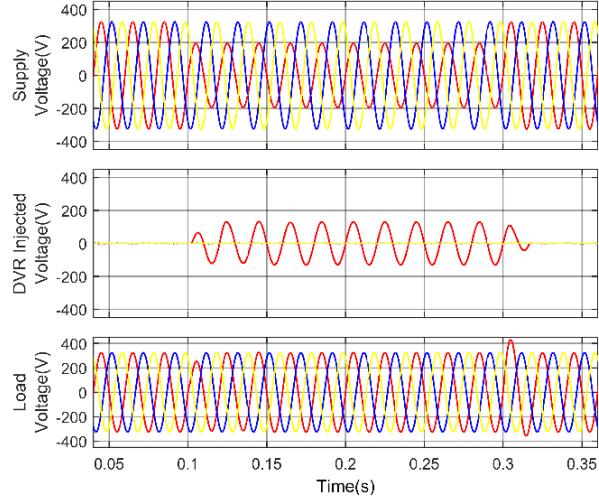


Fig.4 Performance of proposed LV DVR during voltage sag in phase A

injection voltage and the maximum load current possible.

### III. CONTROL OF THE PROPOSED LV DVR

In the proposed three-phase LV DVR, phase locked loop (PLL) is used to create the supply voltage references essential for the d-q co-ordinate system as shown in Fig.2 [8]. The instantaneous values of the three-phase supply voltage reference ( $V_{s,ref}$ ) and actual supply voltage ( $V_s$ ) are continuously compared and a flag equal to 1 is generated whenever the difference goes non-zero. The flag generated brings the proposed DVR in the sag affected phase to compensation mode from stand-by mode by opening the bypass switch. The difference three-phase voltage  $V_{DVR,ref}$  generated as mentioned above is the required compensation voltage or the reference DVR voltage. The SIZS inverter starts synthesizing the required compensation voltage when the sag or swell is detected. The modified feedback controller is utilized which takes the actual DVR voltage  $V_{DVRX}$  as the feedback parameter [9]. This control strategy offers better dynamic response over the feed-forward control.

In the modified feedback control as shown in Fig.3 (given for phase X), the reference DVR voltage  $V_{DVRX,ref}$  and the actual DVR output  $V_{DVRX}$  are compared, and the error  $V_{err}$  is minimized using a PI controller. The output of the PI

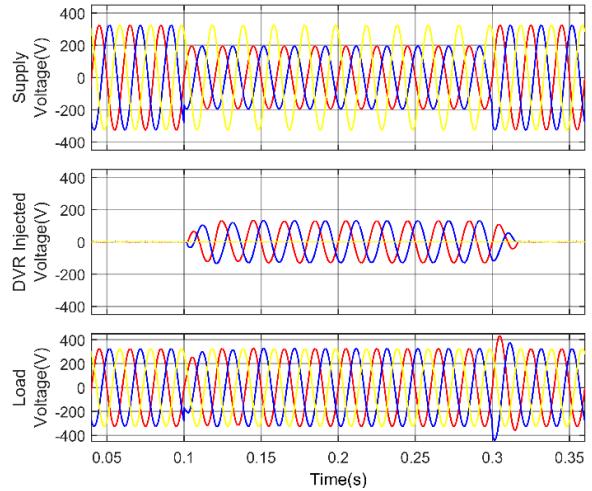


Fig.5 Performance of proposed LV DVR during asymmetrical voltage sag.

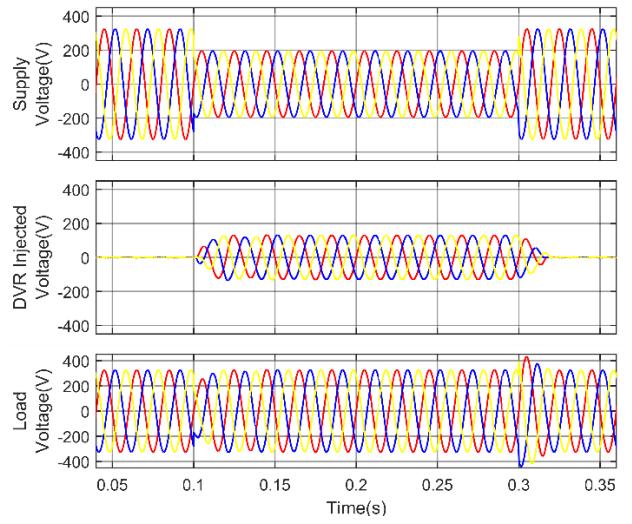


Fig.6 Performance of proposed LV DVR during symmetrical voltage sag

controller is analogous to the modulation index given by  $M_X$ . The PI controller parameters  $K_{pX}$  and  $K_{iX}$  are designed using trial and error method. As the first step for obtaining the modified reference sinusoidal voltage required in the MSPWM, the output of the PI controller  $V_m$  is multiplied by  $\sin \omega t$  which phase-locks with actual supply voltage.

The modified reference signal given by equation (3) is developed by conducting mathematical operations on the obtained  $V_m \sin \omega t$ . The developed modified reference signal is compared with the high-frequency triangular wave and the switching pulses for switch  $S_{2X}$  are obtained. The switching pulses for the switch  $S_{1X}$  is given by complementing the pulses of  $S_{2X}$ . In modified feedback control method, the SZS inverter is always operated appropriately to tightly track the reference DVR voltage.

#### IV. SIMULATION RESULTS

This section presents the simulation results in MATLAB/Simulink platform for validating the feasibility of the novel LV DVR. The system parameters are designed as discussed in the previous section and is given in Table I. The proposed LV DVR is capable of restoring the load voltage during abnormal supply voltage conditions. Fig.4 shows the compensation of load voltage by the proposed three-phase LV DVR during voltage sag in phase A. The phase A voltage is subject to a sag of depth 40% during the time interval from 0.1s to 0.3s. As soon as the sag is detected, the SZS inverter initiates the operation and synthesizes the required compensation voltage. The DVRs connected to the other phases B and C remain in the standby mode during this time interval. The operation of DVR in phase A does not affect the DVR present in B and C. Fig. 5 shows the simulation of the proposed LV DVR compensating asymmetrical sag present in phase A and B. The by-pass switch ( $S_3$ ) in the phase C remains closed even when the other phase DVRs are compensating the sag present in the system. The independent control of the DVRs present in each phase makes possible the smooth mitigation of asymmetrical sags. Even though symmetrical sags occur very rarely, the restoration of load voltage under this abnormal condition is also possible with the proposed LV DVR. The compensating voltages for each phase ( $V_{DVRX}$ ) are supplied by the respective DVRs and the sag is mitigated completely as shown in Fig.6. Fig.6 illustrates the symmetrical sag compensation by the proposed DVR for

swell events, the voltages with magnitude equal to the swell depth, but phase-opposed are generated and added in series with the supply and the load. Fig. 7 shows the restoration of load voltage during a swell event of depth 40% from time 0.1 s to 0.3s. The proposed DVR is also suitable for mitigating swells in single phase, two phases and all the three phases. From the simulation results presented so far with the connected linear load, it is clear that the SZS inverter is suitable for both sag and swell mitigation in the low voltage three-phase power system. It is applicable for non-linear loads also. A non-linear load of diode bridge rectifier is connected to the three-phase supply system. When the non-linear load is met with voltage sag of 40% in phase A from time 0.1s to 0.3s, the SZS inverter completely restores the load voltage to its presag values as presented in Fig.8. The proposed three-phase LV DVR is suitable for the load compensation during voltage sag and voltage swell. The SZS inverter based LV DVR can be used for both linear and non-linear loads. It offers complete regulation of load voltage during abnormal supply conditions such as voltage sag and swell. The proposed LV DVR is the cost-effective alternative for the conventional FB inverter based DVR.

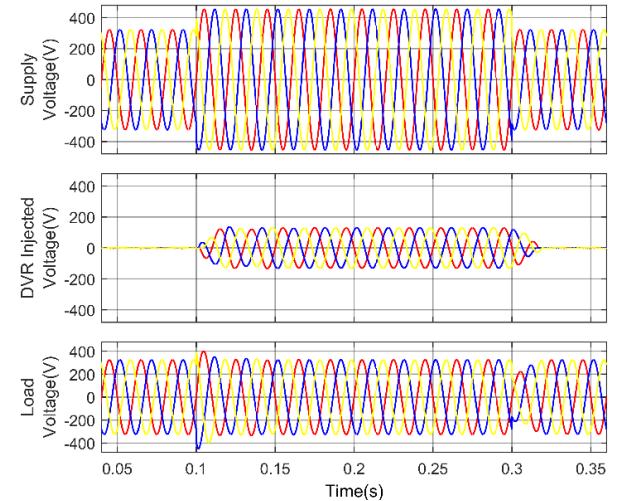


Fig.7 Performance of proposed LV DVR during voltage swell.

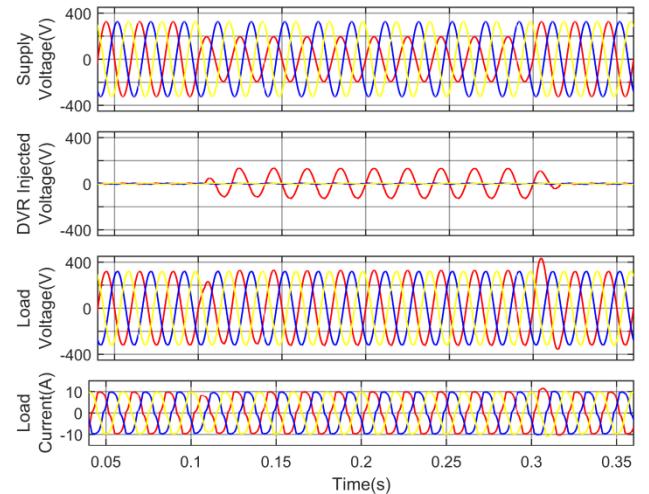


Fig.8 Performance of proposed LV DVR during voltage sag with a non-linear load connected.

40% sag in all the three phases for the time interval 0.1s to 0.3s.

The proposed LV DVR employing SZS inverter is suitable for the mitigation of voltage swells also. During the

## V. CONCLUSION

A novel topology of three-phase LV DVR is proposed in this paper. The proposed topology employing SZS inverter reduces the overall footprint and the number of switches in each phase without compromising the compensation capability when compared to the conventional FB inverter based three-phase DVR. The short-circuiting of supply voltage, dead-time control and filter circuit essential in FB inverter based three-phase DVR are not required in the proposed DVR topology. The independent control of each phase makes the proposed three-phase LV DVR superior to the three-phase inverter based DVR in the distribution network. The d-q co-ordinate system is used to detect the sag and modified feedback control employed helps the DVR to effectively track the reference DVR voltage. The simulation results illustrate the feasibility and the practicality of the proposed LV DVR to restore load voltage of a three-phase system during symmetrical and unsymmetrical sag and swell. The linear and non-linear loads connected to the three-phase system can be protected effectively using the proposed LV DVR topology.

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## REFERENCES

- [1] J. G. Nielsen and F. Blaabjerg, "A Detailed Comparison of System Topologies for Dynamic Voltage Restorers," *IEEE Trans. Ind. Appl.*, vol. 41, no. 5, pp. 1272–1280, Sep. 2005.
- [2] M. Vilathgamuwa, A. A. D. Ranjith Perera, S. S. Choi, and K. J. Tseng, "Control of energy optimized dynamic voltage restorer," in *IECON'99. Conference Proceedings. 25th Annual Conference of the IEEE Industrial Electronics Society*, San Jose, CA, USA, 1999, vol. 2, pp. 873–878.
- [3] J. G. Nielsen, F. Blaabjerg, and N. Mohan, "Control strategies for dynamic voltage restorer compensating voltage sags with phase jump," in *APEC 2001. Sixteenth Annual IEEE Applied Power Electronics Conference and Exposition*, Anaheim, CA, USA, 2001, vol. 2, pp. 1267–1273.
- [4] N. H. Woodley, L. Morgan, and A. Sundaram, "Experience with an inverter-based dynamic voltage restorer," *IEEE Trans. Power Deliv.*, vol. 14, no. 3, pp. 1181–1186, Jul. 1999.
- [5] K. Chan, A. Kara, and D. Westermann, "Integrated gate commutated thyristor based dynamic voltage restorer," in *POWERCON '98. 1998 International Conference on Power System Technology. Proceedings*, Beijing, China, 1998, vol. 1, pp. 635–638.
- [6] V. K. Remya, P. Parthiban, V. Ansal, and A. Nandakumar, "Single-Phase DVR with Semi-Z-Source Inverter for Power Distribution Network," *Arab. J. Sci. Eng.*, vol. 43, no. 6, pp. 3135–3149, Jun. 2018.
- [7] Nambo Hur, Kwanghee Nam, and Sangchul Won, "A two-degrees-of-freedom current control scheme for deadtime compensation," *IEEE Trans. Ind. Electron.*, vol. 47, no. 3, pp. 557–564, Jun. 2000.
- [8] J. G. Nielsen, M. Newman, H. Nielsen, and F. Blaabjerg, "Control and Testing of a Dynamic Voltage Restorer (DVR) at Medium Voltage Level," *IEEE Trans. Power Electron.*, vol. 19, no. 3, pp. 806–813, May 2004.
- [9] J. Godsk Nielsen, "Design and control of a dynamic voltage restorer," Institute of Energy Technology, Aalborg University, Aalborg, 2002.
- [10] D. Cao, S. Jiang, X. Yu, and F. Z. Peng, "Low-Cost Semi-Z-source Inverter for Single-Phase Photovoltaic Systems," *IEEE Trans. Power Electron.*, vol. 26, no. 12, pp. 3514–3523, Dec. 2011.
- [11] T. Ahmed, T. Soon, and S. Mekhilef, "A Single Phase Doubly Grounded Semi-Z-Source Inverter for Photovoltaic (PV) Systems with Maximum Power Point Tracking (MPPT)," *Energies*, vol. 7, no. 6, pp. 3618–3641, Jun. 2014.
- [12] S. Mekhilef and T. Ahmed, "Semi-Z-source inverter topology for grid-connected photovoltaic system," *IET Power Electron.*, vol. 8, no. 1, pp. 63–75, Jan. 2015.
- [13] Wang Jing, Xu Aiqin, and Shen Yueyue, "A survey on control strategies of dynamic voltage restorer," in *2008 13th International Conference on Harmonics and Quality of Power*, Wollongong, NSW, 2008, pp. 1–5.
- [14] M. Farhadi-Kangarlu, E. Babaei, and F. Blaabjerg, "A comprehensive review of dynamic voltage restorers," *Int. J. Electr. Power Energy Syst.*, vol. 92, pp. 136–155, Nov. 2017.
- [15] A. K. Sadigh and K. M. Smedley, "Review of voltage compensation methods in dynamic voltage restorer (DVR)," in *2012 IEEE Power and Energy Society General Meeting*, San Diego, CA, 2012, pp. 1–8.