# Recent Developments in Control Schemes of BLDG Motors

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Abstract- This paper presents a technical review of published literature addressing control schemes for BLDC motors. The control methods reviewed include sensor less control, PWM techniques used, various methods for rotor position detection and initial rotor position detection methods.

### I. INTRODUCTION

Brushless DC motors are widely used as small High Power (HP) control motor and increasingly user for larger HP applications such as hybrid vehicles. In DC commutator motor, current polarity is altered by commutator and brushes. In the brushless DC motor polarity reversal is performed by power transistors switching in synchronization with the rotor position. To accomplish this, BLDC motor is inverter fed. Inverter is Fig.1. Inverter configuration and current commutation sequence for designed in such a way that, its out put frequency is function of BLDC motor instantaneous rotor speed and its phase control will correspond to actual rotor position. II. REVIEW OF SENSORLESS CONTROL METHODS

Typically <sup>a</sup> BLDC motor is driven by <sup>a</sup> <sup>3</sup> phase inverter with six step commutation. In the conventional approach,  $120^\circ$  In the conventional control methods, rotor position is<br>pWM method is used where the conducting interval of each detected every 60 electrical degrees. Based on pr PWM method is used where the conducting interval of each detected every 60 electrical degrees. Based on phase is 120% algorized angle as shown in Fig. 1. In order to operation, these methods are grouped as follows: phase is 120° electrical angle as shown in Fig 1. In order to operation, these methods are grouped as follows:<br>and the second-contract the contract the commutation of the second the second the second the start of the back produce maximum torque, inverter should be commutated  $1.$  Using the back EMF of motor[6 produce maximum torque, inverter should be commutated  $7,8,9,10,11,12,13,14,15$ ] every  $60^{\circ}$ , so that the current is in phase with the back emf.<br>Commutation timing is determined by the nature assisting and  $\frac{7,8,9,10,11,12,13,14,15}{2}$ . Commutation timing is determined by the rotor position and<br>the commutation of conducting state of  $\epsilon$  freewheeling is at the general space of  $\epsilon$  in the unexcited phase [16] the sequence of commutation is retained in the proper order so  $\frac{m}{2}$ . Stator third harmonic components [17] that the inverter performs the function of brush and commutator in a conventional DC motor to generate a continual basis. Therefore not applicable where high estimation rotational stator flux. At one time instant, only 2 out of the 3 accuracy of position is required. phases are conducting current, and one winding is floating.

The conventional  $120^{\circ}$  PWM method has merit of low A. Direct back EMF detection for sensorless BLDC drives<br>itching losses in the inverter side but posse's high harmonic Sensing back EMF of unused phase is the most cos switching losses in the inverter side but posse's high harmonic<br>content This results in increase in loss on the motor side [1, 2] efficient method to obtain the commutation sequence in star content. This results in increase in loss on the motor side  $\begin{bmatrix} 1, 2 \end{bmatrix}$  efficient method to obtain the commutation sequence in star<br>3.4 and 51. Conventionally, three Hall sensors were used as wound motors. Here th 3, 4, and 5]. Conventionally, three Hall sensors were used as wound motors. Here the emmot the floating phase is sensed and received and the floating phase is sensed and received and the floating phase is sensed and receiv position sensors to perform current commutations every 60 the zero crossing of this emit detected by comparing with electrical degrees. Resolver or absolute encoders were used as neutral point voltage. In most cases (Fig 2) it becomes rotor position sensors for servo drive applications. All sensors necessary to build virtual neutral point [6, 7]. This scheme increase the cost, size of motor and reduce reliability. For this reason BLDC motor without position or speed sensors is



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These methods do not provide rotor position estimation on

and high frequency noise due to the PWM drive. So, it requires<br>low pass filters and voltage dividers. Filter will introduce becoming more popular.<br>commutation delay at high speeds and attenuation causes



speed range is narrowed. In order to reduce switching noise Neglecting forward voltage drop across the diode for the phase<br>back EMF integration and third harmonic integration methods A and voltage drop across the device fo

a back EMF sensing method which does not require a virtual voltage is referred to ground instead of neutral point. neutral point and large amount of filtering. Here the zero As a result there are no common mode voltage issues. since crossing of emf of floating phase is obtained by properly true back emf is extracted from the terminal voltage zero<br>selecting the PWM and sensing strategy. In the scheme selecting the PWM and sensing strategy. In the scheme crossing of the back EMF can be detected very precisely. proposed by Jianwen Shao et.al [6] PWM signal is applied on Resulting signal is not attenuated or filtered and there by high side switches only, low side switches are only switched to provides a signal with good signal/noi provide commutation as in Fig 3. At any instant one phase is provides a much wider speed range.<br>
driven with PWM high side switch and another phase is driven with the low side switch. Remaining phase is open. Back emf and another phase is open. Back emf open phase is detected during PWM off time.



Fig.3. PWM applied to high side switches only



Fig <sup>4</sup> .Winding terminal voltage during PWM off time

Fig 4 shows a particular stage where phase A and B are  $V_{\text{virtual ground}}$  divider & filter  $\ell$  conducting and phase C floating. Upper switch A is pulse width modulated and lower switch of phase B is on during the Fig 2 .Back EMF sensing based on virtual neutral point entire step, switched only at commutation. When upper switch reduction in signal sensitivity at low speeds. Consequently of phase A is turned off current freewheels through the diode.<br>Speed range is narrowed. In order to reduce switching noise Neolecting forward voltage drop across A and voltage drop across the device for phase B we can get are used [9] Also Various PWM techniques are developed.  $V_c=e_c+V_n=e_c(3/2)$ . During PWM off time terminal voltage of the floating phase V, is proportional to back EMF e, with some 1) PWM technique which eliminates virtual ne 1) PWM technique which eliminates virtual neutral point the floating phase  $V_c$  is proportional to back EMF  $e_c$  with some Jianwen Shao et al [6] and T.Endo et...al. [8] have presented an without any superimposed switchin gain without any superimposed switching noise. Also terminal

provides a signal with good signal/noise ratio. This scheme

body diode of the MOSFET's will affect the performance. When the motor speed goes low, zero crossing is not evenly distributed. If the speed goes further low, the back emf  $A + ||$  amplitude becomes too low to detect. Two methods to correct A-lection the offset voltage of back EMF signal are presented by Jianwen Shao et al. [10]. First method is to use complementary PWM as shown in Fig 5. This also reduces the conduction loss. Another method to eliminate the effect of diode voltage drop is to add a constant voltage to compensate the effect of diode and threshold voltage for zero crossing detection. Preconditioning circuits for low speed applications is also presented in [10], which not only compensates the offset voltage caused by diodes but also amplifies the signal of back EMF near zero crossing.

Jianwen Shao et al. [11] have proposed an improved direct back EMF detection for sensorless control of BLDC motor.



Fig.5. Complementary PWM algorithm



This scheme eliminates the limitation of back EMF detection as shown in Fig 8.<br>This scheme eliminates the limitation of back EMF detection as shown in Fig 8. during PWM off time. That is, it cannot go up to 100% duty During the period when high side device is with chopper cycle since minimum off time is needed to have a time window control the associated low side switch is trig cycle since minimum off time is needed to have a time window to detect back emf. Here back EMF is detected during PWM signal of chopper control. The on state of low side power<br>on time for some applications where 100% duty ratio is device indicates that the output terminal is connect on time for some applications where  $100\%$  duty ratio is necessary. At lower speed, back EMF detection is done during negative dc link rather than the positive. The low side power<br>off time. Fig. 6 shows the winding terminal voltage during switch is also controlled in a similar m off time. Fig 6 shows the winding terminal voltage during PWM on time when phase A and B are conducting current and chopper control, the associated high side switch is triggered by phase C is floating. Under this condition terminal voltage of inverse signal of chopper control sig phase C is floating. Under this condition terminal voltage of inverse signal of chopper control signal. For the other two<br>open phase C is  $V = \epsilon_0 (3/2) + V_{\phi}/2$ . Comparing V<sub>e</sub> with  $V_{\phi}/2$  phases control signals are appli open phase C is  $V_c = e_c (3/2) + V_{dc}/2$ . Comparing  $V_c$  with  $V_{dc}/2$  phases control signals are applied with 120° phase shift. With gives zero crossing of back emf e. Thus duty cycle limitation this technique, voltage drop cau gives zero crossing of back emf  $e_c$ . Thus duty cycle limitation this technique, voltage drop caused by the turn on resistance of can be overcome by synchronously detecting the back  $EMF$  power device and the load current, can be overcome by synchronously detecting the back EMF during the PWM on time. **compared** to forward voltage drop of diode. Hence results in

## 4) PWM technique for small power applications small power applications

A novel PWM technique for small power BLDC motor<br>ives which reduces the conduction losses and in turn reduces *B. DSP based Sensorless control for high speed applications* drives which reduces the conduction losses and in turn reduces  $B$ heat dissipation presented in [12,13]. For small power Based on the method of executing the PWM control, in Fig 7. Similar control signals are given to low side devices upper switch PWM, lower switch PWM schemes. with 180° shift.



 $V_a$   $W_{\text{w}}$   $\longrightarrow$   $W_{\text{w}}$   $W_{\text{w}}$   $\longrightarrow$   $W_{\text{w}}$   $W_{\text{w}}$   $\longrightarrow$   $W_{\text{w}}$  However as the low side device is on , output terminal is connected to the negative dc link. For other two phases the control signals are applied with  $120^{\circ}$  shift.

### 5) Improved PWM technique for small power applications

 $W^{\text{th}}\text{O}^{-1}$  The significant heat loss that is produced due to the current flowing through the anti-parallel diode during the period when the switch is with chopper control(Fig 7) is reduced in another PWM technique given in [13]. In this PWM technique high Fig 6 . Winding terminal voltage during PWM on time side switch is chopped in 1/6 fundamental frequency and clamped to DC link for in the next 1/6 fundamental frequency

reduction in power consumption and method is promising for

applications of BLDC drives power consumption reduction is schemes are classified as uni polar and bipolar switching the main objective because of the use of battery and limited schemes. In uni polar switching method PWM is applied to one space for heat dissipation. In the PWM technique presented by of the two active switches in on state while the other switch Yen-Shin Lai et al. [12] the high side power device is chopped remains ON state [6, 10]. In bipolar switching scheme both the in 1/6 fundamental period, duty ratio is derived from the speed active switches are applied with PWM at the same time [7, 8, reference or error of speed. For the next 1/6 fundamental 12, 13, and 14]. Unipolar switching scheme has the advantage period, it is clamped to positive dc link for both intervals of of reduced switching loss. Unipolar switching is further high side device, the associated low side device is off as shown classified into on going phase PWM, off going phase PWM,



Fig 8 PWM scheme promising for low power applications Commutation signal interrupt

In the on going switch PWM scheme each switch executes commutation delay the PWM during the first 60 degrees of active interval and held (b) on during the second 60 degrees of interval [13]. In the off going PWM each active switch is held on during the first 60 Fig.9. Relation between the PWM switching period and commutating instant degrees of active interval and applied with PWM in the next 60 (a) Ideal commutation.(b)Case of commutation delay degrees [16]. In the upper switch PWM scheme, PWM is given to upper one of the two active switches and in the lower switch PWM vise versa [6, 10].

Depending on the PWM method used, the control scheme may cause a commutation delay in high speed applications, since the PWM switching and the inverter commutation cannot be done independently. If the commutating instant is  $\left|\frac{1}{2}\right| \leq \left|\frac{1}{2}\right|$ ,  $\left|\frac{1}{2}\right|$ ,  $\left|\$ synchronized with the end of the PWM switching period, ideal commutation occurs with out any delay. But, since the commutating instant depends on the rotor position, it does not generally coincide with the end of PWM period. In such cases undesirable commutation delay is produced, if the commutation is performed with the end of the present  $PWM$  Fig 10. Circuit configuration for a DC link voltage control scheme period as shown in Fig 9. One way of avoiding this delay is to terminate the present PWM period and synchronize a new Commutation signals and actual speed are obtained using the PWM period with the commutation instant. But this may cause PWM period with the commutation instant. But this may cause sensed back EMF by means of integration and comparison<br>an irregular switching frequency in upper and lower switch circuits. Heing a digital PL controller and the an irregular switching frequency in upper and lower switch circuits. Using a digital PI controller and the calculated value<br>PWM schemes. In on going and off going phase PWM of speed duty ratio of chapper is controlled With schemes this method can be used for high speed sensorless phase PWM method can be used even in high speed region control. However, only few pulses of PWM can be used for without any commutation delay. control. However, only rew pulses of PWM can be used for without any commutation delay.<br>speed control during a 60 degree interval in high speed range. Commutation delay can be reduced by increasing the PWM C. Microprocessor based sensoriess controllers<br>switching frequency But there is a practical limitation on the Microprocessor are playing major role in building the switching frequency. But there is a practical limitation on the Microprocessor are playing major role in building the  $\frac{M}{n}$  control scheme for sensorless control of switching frequency due to the increased switching losses, controllers. In [15], a control scheme for sensorless control of  $_{\rm{PLDC}}$  motor is presented which requires heat. EME sensing switching frequency of commercially available power devices BLDC motor is presented which requires back EMF sensing is less than 20 kHz. These problems are over come by from only one of the EMF of 3 phase motor.<br>controlling the voltage and frequency independently by DC The signal sensed is fed into an integrator as shown in Fig 11 controlling the voltage and frequency independently by DC link voltage control scheme.

DC link voltage control is presented by Kyeong-Hwa Kim et al<br>[14] Hara the inverter is supplied with a square wave of 120 pass filter is used to extract the phase information from the [14]. Here the inverter is supplied with a square wave of  $120$  pass filter is used to extract the phase information from the degree conducting interval whose frequency is controlled and back EMF as in Fig12 since an idea degree conducting interval whose frequency is controlled and speed control is obtained by regulating the DC link voltage of the inverter shown in Fig 10 fed from a step down chopper. Rotor position information is detected using the back EMF.





of speed, duty ratio of chopper is controlled . With this two

### C. Microprocessor based sensorless controllers

from only one of the EMF of 3 phase motor.

for filtering and introducing necessary delay. Then the signal is<br>fed to zero crossing detector which produces two A DSP based high speed sensorless control scheme using a fed to zero crossing detector which produces two<br>Clink voltage control is presented by Kyeong-Hwa Kim et all commutation instants per fundamental cycle. In practice In that case phase delay introduced by the filter varies with the motor speed and has to be corrected in order to produce correct commutation timing. The output of zero crossing detector is







elapsed between two instants and generates the other two rotate. When the stator field becomes just strong enough rotor<br>commutation instants by interpolation. This leads to a can move in any direction. Disadvantage of the commutation instants by interpolation This leads to a can move in any direction. Disadvantage of the method is that<br>significant reduction in components of position sensing circuit initial rotor movement cannot be predicted significant reduction in components of position sensing circuit initial rotor movement cannot be predicted. Also if the stator<br>and thereby considerable cost saving due to coupling of field is too large then the rotor will and thereby considerable cost saving due to coupling of  $\frac{\text{field}}{\text{cell}}$  is too large the rotor will be subjected to a single objected to rotor will be subjected to retain the subjected to rotor will be subjected to rotatio sensing circuit to a single chip microprocessor or DSP for speed control as shown in Fig 13. In [18, 19] rotor position is estimated using Kalman filter

## III. INDIRECT BACK EMF DETECTION USING STATOR THIRD In [21,22, and 23], an initial position detection technique is<br>HARMONIC COMPONENTS presented which does not require current and position sensors

commutation advance or the current decay in freewheeling value of stator inductance which  $\frac{1}{\sqrt{2}}$  used for rotor position detection. diode is greater than 30 electrical degrees. In such case third Many methods are proposed for detection of rotor position

### IV. INDIRECT BACK EMF DETECTION BY DETECTING THE CONDUCTING STATE OF FREE WHEELING DIODES

In [16], position information detection method based on the<br>conducting state of diodes in the open phase has been Integrator Zero- Crossing conducting state of diodes in the open phase has been <sup>d</sup> Sa S~~~~~~mMicro cfo~ <sup>r</sup> =presented. Current flowing in the open phase results due to processor back emf produced in the motor windings. This current becomes zero in the middle of commutating interval. Position Gating signals information is obtained every 60 degrees by detecting the Fig 11 Rotor position sensing circuit using only one motor terminal voltage conducting interval of free wheeling diodes. Detected position signal leads the next commutation by 30 degrees hence the commutation signal to the inverter is given through a phase shifter. This approach makes it possible to detect the rotor Gating position over a wide speed range. This is an indirect detection of back EMF through free wheeling diodes which requires  $\mathsf{p}$  sinals additional power supplies for the comparator circuitry for each

In  $[18, 19, and 20]$ , a method for continual estimating the Crossing rotor position and speed is presented. This method is based on application of Extended Kalman Filter. Motor state variables Fig12. Rotor position sensing circuit using low pass filter are are estimated by using measurements of stator line voltages and currents and applying EKF. During this process, voltage and current measuring signals are not filtered. Rotor position and speed can be estimated with sufficient accuracy in both steady state and dynamic operations.

PM PM Accurate position and speed feed back information for  $\text{Wotor}$  and  $\text{U}$  is the closed loop control based on vector control technique is presented in [20].Here Kalman filter is used for mechanical state estimation of the motor.

### V. INITIAL ROTOR POSITION DETECTION TECHNIQUES OF BLDC MOTORS

In order to avoid wrong direction of rotation at start which is essential for particular applications like electric vehicle, initial Microprocessor <br>position of the permanent magnet should be identified clearly.

Open loop starting is achieved by providing a rotating stator Fig13. Position and sensorless control example field that is gradually increasing in magnitude and/or fed to a microprocessor. Microprocessor measures the time frequency. Rotor gets attracted to the stator field and begins fed to a microprocessor. Microprocessor measures the time frequency. Rotor gets attracted to the stator field and begins to elapsed between two instants and generates the other two rotate. When the stator field becomes jus

from measured stator line voltages and currents.

presented which does not require current and position sensors.

In [17] sensorless control of brushless machines by detecting Basic principle to initial position detection of 3 phase BLDC in the phase BLDC and between rotor position and between rotor position and third harmonic back EMF has been presented which is motor is based on the relationship between rotor position and<br>applicable for the operation in flux weakening mode Methods inductance of stator windings. Inductance of sta applicable for the operation in flux weakening mode. Methods inductance of stator windings. Inductance of stator winding for<br>hased on zero crossing of bock EME are simple but applicable in a saturated case (when the field based on zero crossing of back EMF are simple but applicable non saturated case (when the field of stator and rotor are with under normal approximations. It cannot be applied if  $180^{\circ}$  phase shift) is greater than in th under normal operating conditions. It cannot be applied if  $\frac{180}{180}$  phase shift) is greater than in the saturated case. The

harmonic components of back EMF are used.<br>by indirectly detecting stator inductance [21, 22]. These

include detecting peak value of stator currents, and may be

- i. Current after excitation: Where stator windings are excited for a short period less than  $\tau$  of stator for unsaturated case.<br>
Exposition, 1991<br>
Exposition, 1991<br>
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Exposition, Nember, IEEE, Dennis Nolan and Thomas Hopkins,
- ii. Detecting current magnitude: Magnitude of stator currents are sensed for a given time after saturated case is greater than linear case. Direct Back EMF Detection for Sensor Brushless Brushless Direct Back EMF Detection for Sensors Brushless Brushless Direct Back EMF Detection for Sensors Direct Back EMF Detection
- iii. Detecting rise time of stator winding after [12] Yen-Shin Lai, Fu-San Shyu, Yung- Hsin Chang, "Novel Pulse-Width unsaturated case than linear case. For above three approaches initial position is identified [13] Yen-Shin Lai, Fu-San Shyu, Yung- Hsin Chang, "Novel loss reduction comparing peak, magnitude or response speed of by Mosfet Inverter No. 6, Nov. 2004. currents associated with the exciting signal. No. 6, Nov. 2004.
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